

AMENDMENT TO THE CLAIMS

Please replace all previous versions of the claims with the following rewritten claims listing.

Listing of Claims:

1.-33. (Cancelled)

34. (New) A liquid crystal display device comprising:
a substrate;
a gate driver disposed on the substrate;
a gate line connected to the gate driver and comprising a gate pad;
a data line disposed substantially perpendicular to the gate line;
a thin film transistor connected to the gate line and the data line;
a pixel electrode connected to the thin film transistor; and
a driving signal wire which transmits driving signals from an outside of the substrate to the gate driver and which comprises a first protrusion portion extended toward the gate pad,

wherein the gate pad comprises a second protrusion portion extended toward the driving signal wire and the first protrusion portion is adjacent to and insulated from the second protrusion.

35. (New) The liquid crystal display device of claim 34, wherein the driving signal wire is separated from the gate line, the data line, the thin film transistor, and the pixel electrode.

36. (New) The liquid crystal display device of claim 35, further comprising:
a test pad disposed on the substrate; and
a flexible printed circuit connected to the substrate and comprising a first driving pad,
wherein a first end of the driving signal wire is connected to the test pad and a second end of the driving signal wire is connected to the first driving pad.

37. (New) The liquid crystal display device of claim 36, wherein the gate driver comprises a second driving pad connected the driving signal wire.

38. (New) The liquid crystal display device of claim 37, wherein the driving wire is supplied with a test signal.

39. (New) A liquid crystal display device comprising:

- a substrate;
- a plurality of gate drivers disposed on the substrate;
- a plurality of gate lines respectively connected to the plurality of gate drivers, each of the plurality of gate lines comprising a gate pad;
- a plurality of data lines disposed substantially perpendicular to the plurality of gate lines;
- a plurality of thin film transistors, each of the plurality of thin film transistors connected to one of the plurality of gate lines and one of the plurality of data lines;
- a plurality of pixel electrodes respectively connected to the plurality of thin film transistors;
- a first driving signal wire which transmits driving signals from an outside of the substrate to the plurality of gate drivers, and
- a plurality of first connecting portions disposed between the first driving signal wire and the gate pad of each of the plurality of gate lines,
- wherein a first end of each of the plurality of first connecting portions is connected to the first driving signal wire and a second end of each of the plurality of first connecting portions is connected to the gate pad and each of the plurality of first connecting portions is divided into two parts.

40. (New) The liquid crystal display device of claim 39, further comprising:

- a second driving signal wire which transmits driving signals from an outside of the substrate to the plurality of gate drivers; and
- a plurality of second connecting portions disposed between the second driving signal wire and the gate pad of each of the plurality of gate lines,
- wherein a first end of each of the plurality of second connecting portions connect to the first driving signal wire and a second end of each of the plurality of second connecting portions

connects to the gate pad and each of the plurality of second connecting portions is divided into two parts.

41. (New) The liquid crystal display device of claim 40, wherein the first driving signal wire and the second driving signal wire are separated from the plurality of gate lines, the plurality of data lines, the plurality of thin film transistors, and the plurality of pixel electrodes.

42. (New) The liquid crystal display device of claim 41, further comprising:
a first test pad disposed on the substrate;
a second test pad disposed on the substrate; and
a flexible printed circuit connected to the substrate and comprising a first driving pad and a second driving pad,

wherein a first end of the first driving signal wire connects to the first test pad and a second end of the first driving signal wire connects to the first driving pad and a first end of the second driving signal wire connects to the second test pad and a second end of the second driving signal wire connects to the second driving pad.

43. (New) The liquid crystal display device of claim 42, wherein a distance between the first driving signal wire and the gate pad of each of the plurality of gate lines is smaller than a distance between the second driving signal wire and the gate pad of each of the plurality of gate lines.

44. (New) The liquid crystal display device of claim 43, wherein the plurality of first connecting portions and the plurality of second connecting portions are alternately disposed.

45. (New) The liquid crystal display device of claim 44, wherein the gate driver comprises a third driving pad connected the first driving signal wire and a fourth driving pad connected the second driving signal wire.

46. (New) The liquid crystal display device of claim 45, wherein the first driving wire and the second driving wire are supplied with a test signal.